

FIG. 1

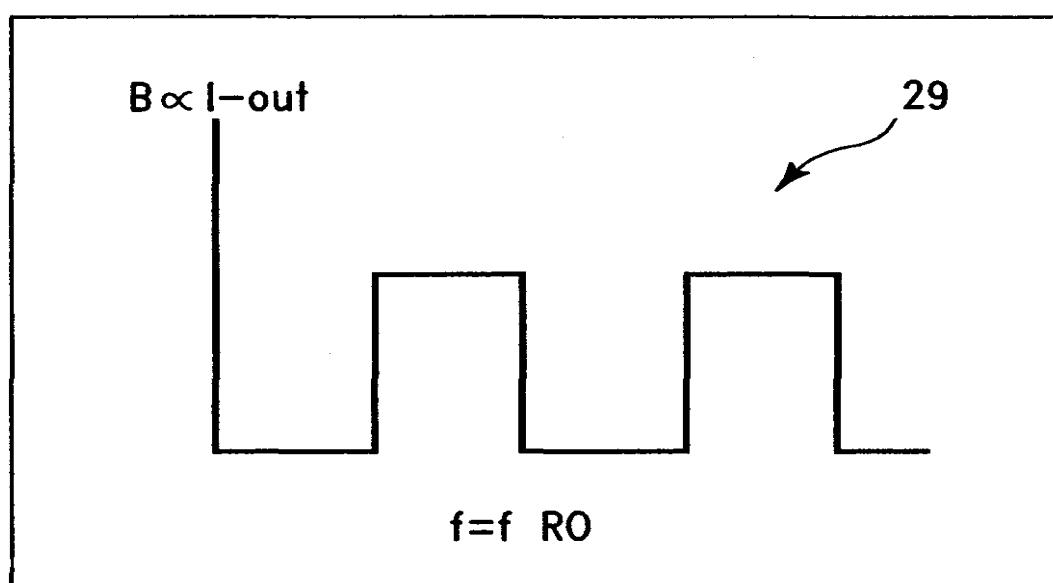


FIG. 2

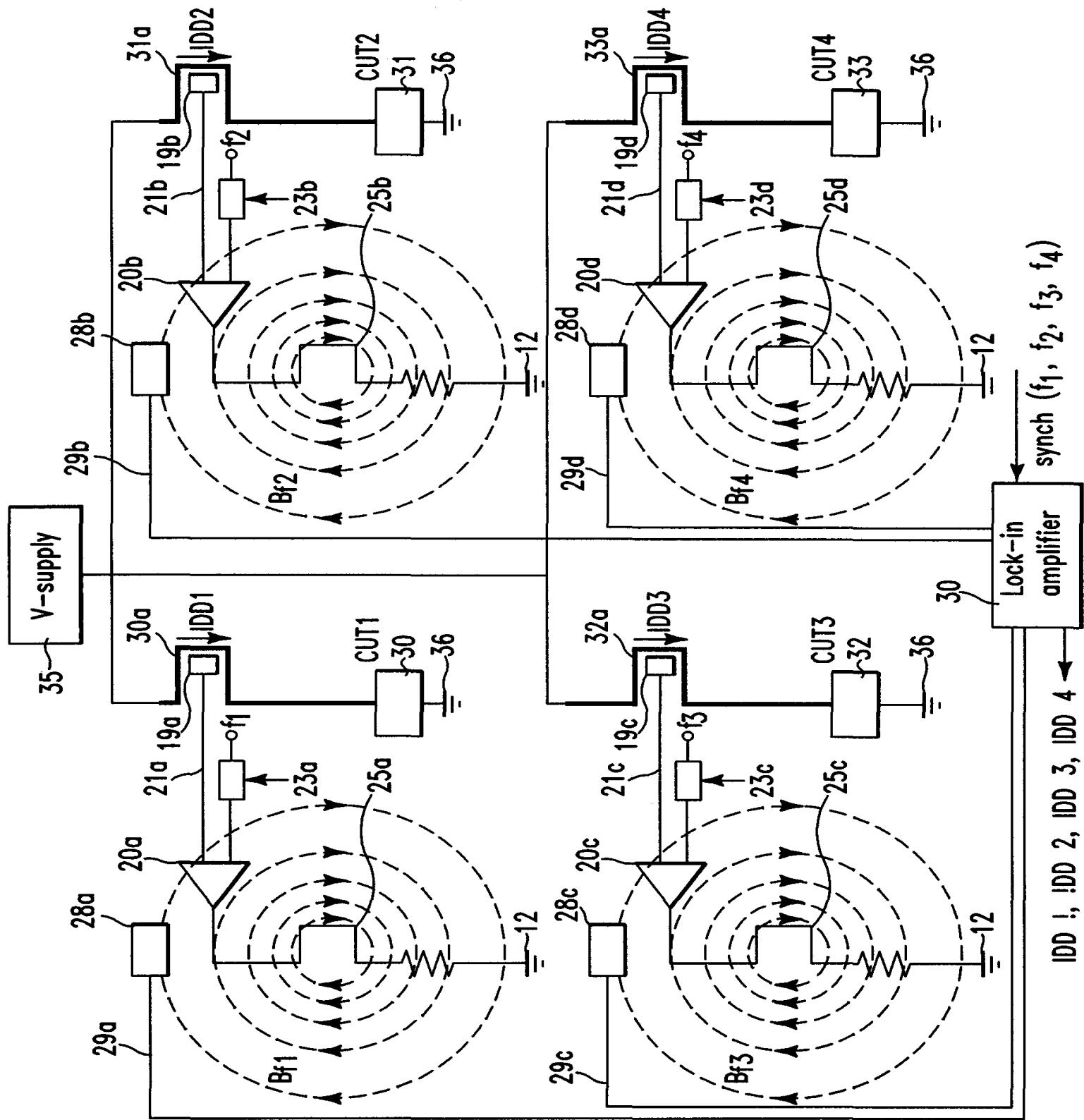
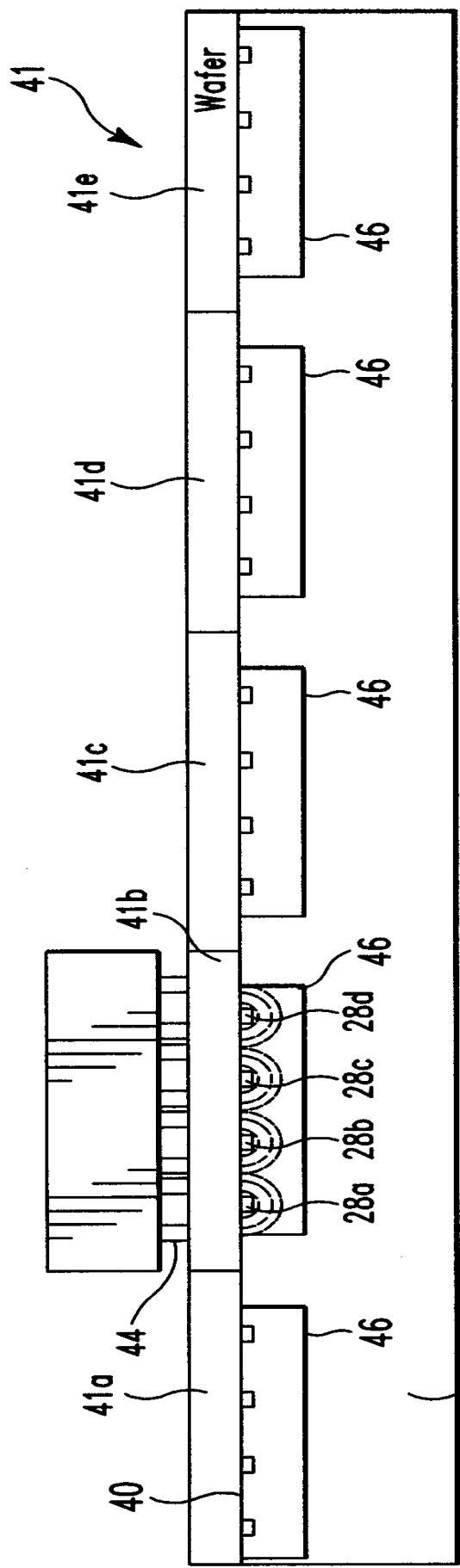


FIG. 3

IDD 1, IDD 2, IDD 3, IDD 4 →  
**synch (f<sub>1</sub>, f<sub>2</sub>, f<sub>3</sub>, f<sub>4</sub>)**



**FIG. 4**

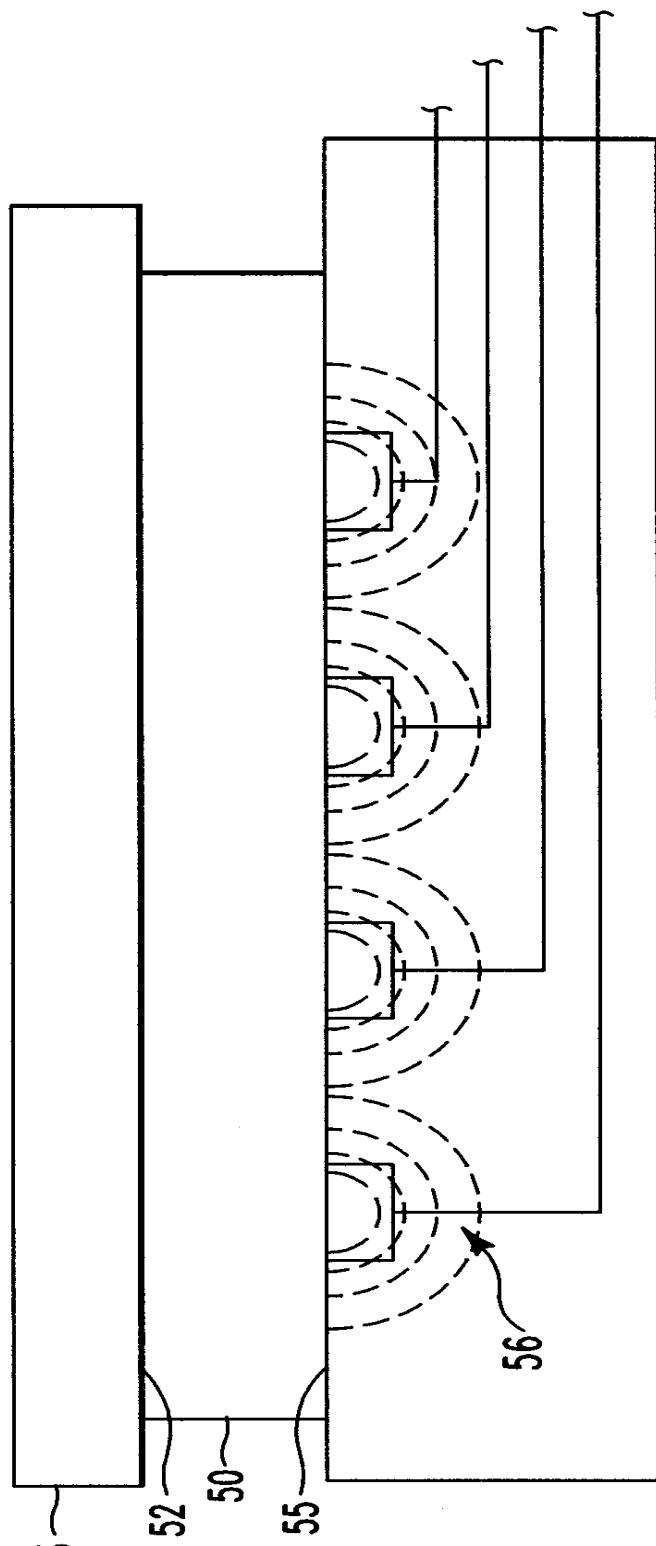


FIG. 6

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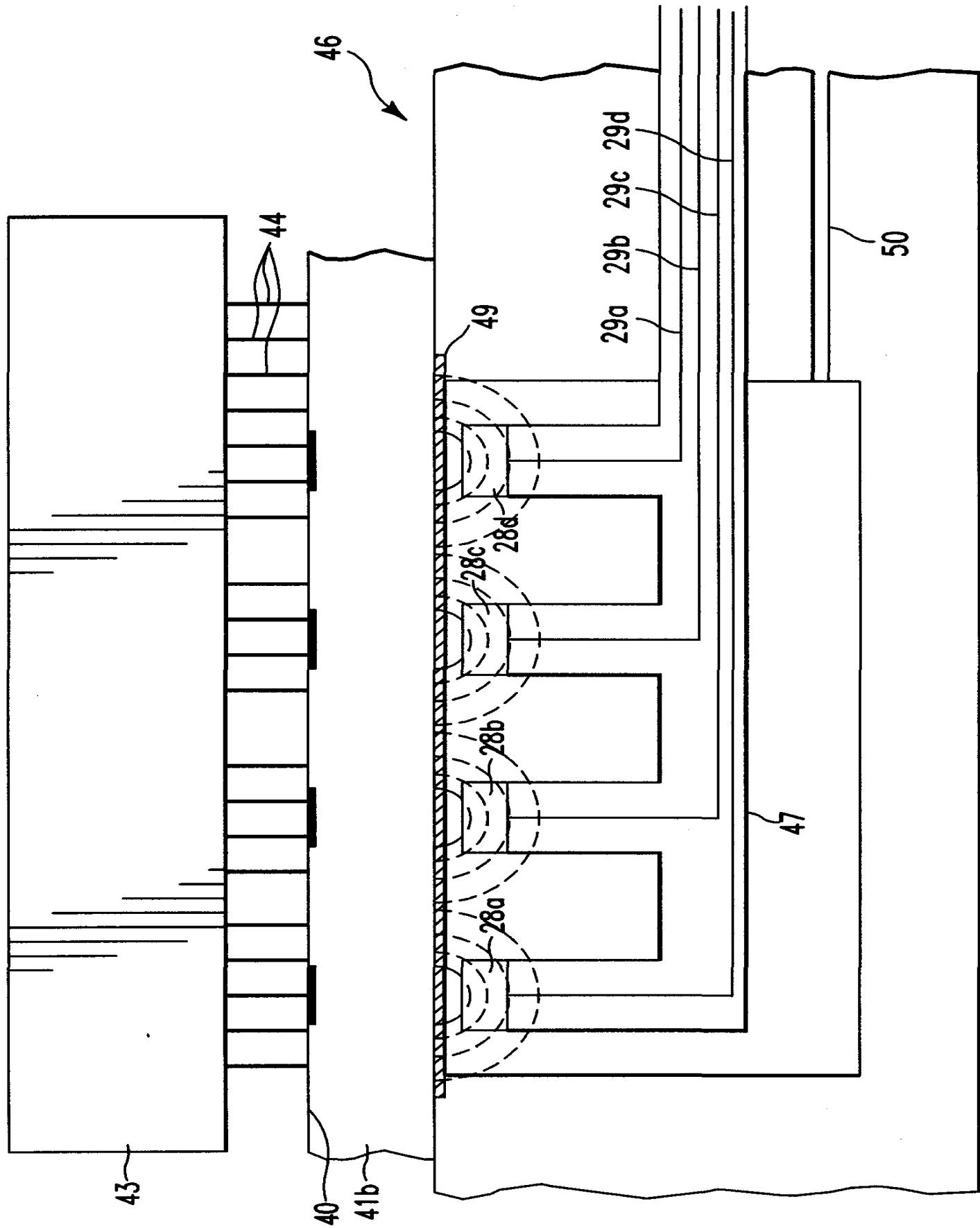


FIG. 5